

ATTORNEY DOCKET NO.
TI-36287 (032350.B591)

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	HARRIS, et al.
Serial No.:	To be Determined
Filing Date:	April 1, 2004
Group Art Unit:	Unassigned
Examiner:	Unassigned
Title:	SYSTEM AND METHOD FOR PREDICTING BURN-IN CONDITIONS

MAIL STOP PATENT APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97, and 1.98, that the documents listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. A copy of each document is enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), Applicants makes no representation that these documents qualify as prior art or that these documents are material to patentability of the present application or that a search has been made.

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PATENT APPLICATION

Since the present Application was filed after June 30, 2003, a copy of any U.S. Patent and any U.S. Patent Application Publication cited on the attached PTO Form 1449 is not being submitted with this Information Disclosure Statement pursuant to the July 11, 2003 waiver of 37 C.F.R. S 1.98(a)(2)(i) by the U.S. Patent and Trademark Office.

Respectfully submitted,

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Date: April 1, 2004

Customer Number:

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Patent Trademark Office

PTO-1449 Information Disclosure Citation In an Application	Application No. To be Assigned	Applicant(s) HARRIS, et al.	
	Docket Number TI-36287 (032350.B591)	Group Art Unit To be Assigned	Filing Date Apr. 1, 04

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
	L						
	M						
	N						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	O							
	P							
	Q							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	R	P. Tadayon, "Thermal Challenges During Microprocessing Testing," Intel Technology Journal, Q3, 2000, (8 pages)	Q3, 2000
	S		
	T		

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE